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```
#BSUB [options]
command [arguments]
```

□□□□ `#BSUB` □□□□□□ `bsub` □□□□□□□□□□□□□□□

> “□□□□□□□□□ e5v3ib □□□□□ 48□□□□□□□□□□□□□□□ MgSiO3 □□□□□□□□□ `out` □□□□□□□□□□□ `err` □ Intel MPI □□□□□ 0
>
> ```
> $ cat job.lsf
> #BSUB -q e5v3ib
> #BSUB -n 48
> #BSUB -J MgSiO3
> #BSUB -o out
> #BSUB -e err
> module load ips/2018u4
> mpirun ./app
>
> $ bsub < job.lsf
> Job <3207099> is submitted to queue <e5v3ib>.
> ```
>
> □□□□□□□□□
>
> ```
> $ bsub -q e5v3ib -n 48 -J MgSiO3 -o out -e err "module load ips/2018u4;mpirun ./app"
> Job <3207099> is submitted to queue <e5v3ib>.
> ```

bsub □□□□

- `-J job_name` □□□□□

□□□□

- `-n min_tasks[,max_tasks]` □□□□□ CPU□□□□□□□□□ `-n 4` □□□ 4~8□□□ `-n 4,8`
- `-m` □□□□□□□□□□□□□□□□

□□□□□□□□ `+[num]` □□□□□□□□□□□□□□ c04n01 □ c04n02 □□ `-m "c04n01 c04n02"` □□□□□ f01n01~n03□□□□□□□ f01n01□□□□□ f01n02`-m "f01n01+2 f01n02+1 f01n03"` □

- `-R "res_req"` □□□□□□□□□□□□□□

- □□□□

- `-w 'ended(job_ID | "job_name")'`□□□ job_ID □ job_name □□□□□□□□□□□ EXIT □ DONE
- □□□□□□□ && (AND)□ || (OR)□ ! (NOT)
- □□□□□□□□□□□□□□□□□ 1□□□□□□□□□

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MPI/OpenMP □□□□

OpenMP (Open Multi-Processing) □□□□□□□□□□□□□□□□□□□□□□□□□□□□□ MPI (Message Passing Interface) □□□□□□□□□□□□□□□□□□□□□□□□□□□□□ OpenMP □□□□□□□□□□□□□□□□□□□□

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- LSB_JOBID□□□ ID
- LSB_QUEUE□□□□
- LSB_JOBNAME□□□□
- LSB_DJOB_NUMPROC□□□ CPU□□□
- LSB_DJOB_HOSTFILE□□□□□□□□□□□□□□
- LSB_HOSTS□□□□□□□□□□□ CPU□□□□□□□□□□□□
- LSB_MCPU_HOSTS□□□□□□□□□□□□□□□□□□ CPU□□□□□

```
LSB_DJOB_NUMPROC=6
LSB_HOSTS="node1 node1 node1 node2 node2 node2"
LSB_MCPU_HOSTS="node1 3 node2 3"
```

```
$ cat $LSB_DJOB_HOSTFILE
node1
node1
node1
node2
node2
node2
```

> “ LSB_HOSTS □ LSB_MCPU_HOSTS □□□□□□□□□□□□□□ LSB_MCPU_HOSTS □ LSB_HOSTS □□□□□□□□ LSB_HOSTS □□ 4096 □□□□□□ LSB_MCPU_HOSTS□

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□□□□□□□□□ e52660 □□□□□□□□□□□□□□□□□

```
$ bsub -q e52660 ./app
Job <3279929> is submitted to queue <e52660>.
$ cat job.lsf
#BSUB -q e52660
./app
```

```
$ bsub < job.lsf
Job <3279930> is submitted to queue <e52660>.
```

MPI □□□□

MPI□□□□□□ `mpirun` □□

□□□□□□ 48□□ Intel MPI □□□□□ e5v3ib□□□□□□□□

```
$ bsub -q e5v3ib -n 48 "module load ips/2018u4;mpirun ./app"
Job <3280120> is submitted to queue <e5v3ib>.
```

□□□□□□ 48□□ Open MPI □□□□□ e5v3ib□□□□□□□

```
$ cat job.lsf
#BSUB -q e5v3ib
#BSUB -n 48
module load iccifort/15.0.3 imkl/11.2.3 openmpi/1.10.0-iccifort-15.0.3
mpirun ./app

$ bsub < job.lsf
Job <3280122> is submitted to queue <e5v3ib>.
```

OpenMP □□□□

OpenMP □□□□□□□□ `-n` □□□□□□□□□□□ **CPU□□**

□□□□□□ 64□□ OpenMP □□□□□ e7v4ib□□□□□□□ -nt □□□□□□□□□□□□□□

```
$ bsub -q e7v4ib -n 64 "./app-nt \$LSB_DJOB_NUMPROC"
Job <3348175> is submitted to queue <e7v4ib>.
```

□□□□□□ 64□□ OpenMP □□□□□ e7v4ib□□□□□□□ `OMP_NUM_THREADS` □□□□□□□□□□□□□

```
$ cat job.lsf
#BSUB -q e7v4ib
#BSUB -n 64
OMP_NUM_THREADS="$LSB_DJOB_NUMPROC"
./app

$ bsub < job.lsf
Job <3348182> is submitted to queue <e7v4ib>.
```

MPI/OpenMP □□□□

□□ MPI□□□ 6□ OpenMP□□

□□□□□□ `OMP_NUM_THREADS` □□

```
#BSUB -q 6140ib
#BSUB -n 72
export OMP_NUM_THREADS=6
source /fs00/software/lsf/misc/ompthreads.sh
module load ips/2018u4
mpirun ./run
```

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```
#BSUB -q 6140ib
#BSUB -n 72
source /fs00/software/lsf/misc/ompthreads.sh 6
module load ips/2018u4
mpirun ./openmx -nt 6
```

GPU □□

□□□□□□ 1□ GPU □□□□ e5v4p100ib □□

```
bsub -q e5v4p100ib -gpu num=1 ./gpu_app
```

□□□□□□ 4□ GPU □□□□ 62v100ib □□□□□ GPU-CPU □□

```
bsub -q 62v100ib -gpu "num=4:aff=yes" ./gpu_app
```

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